



1/4

FIG. 2

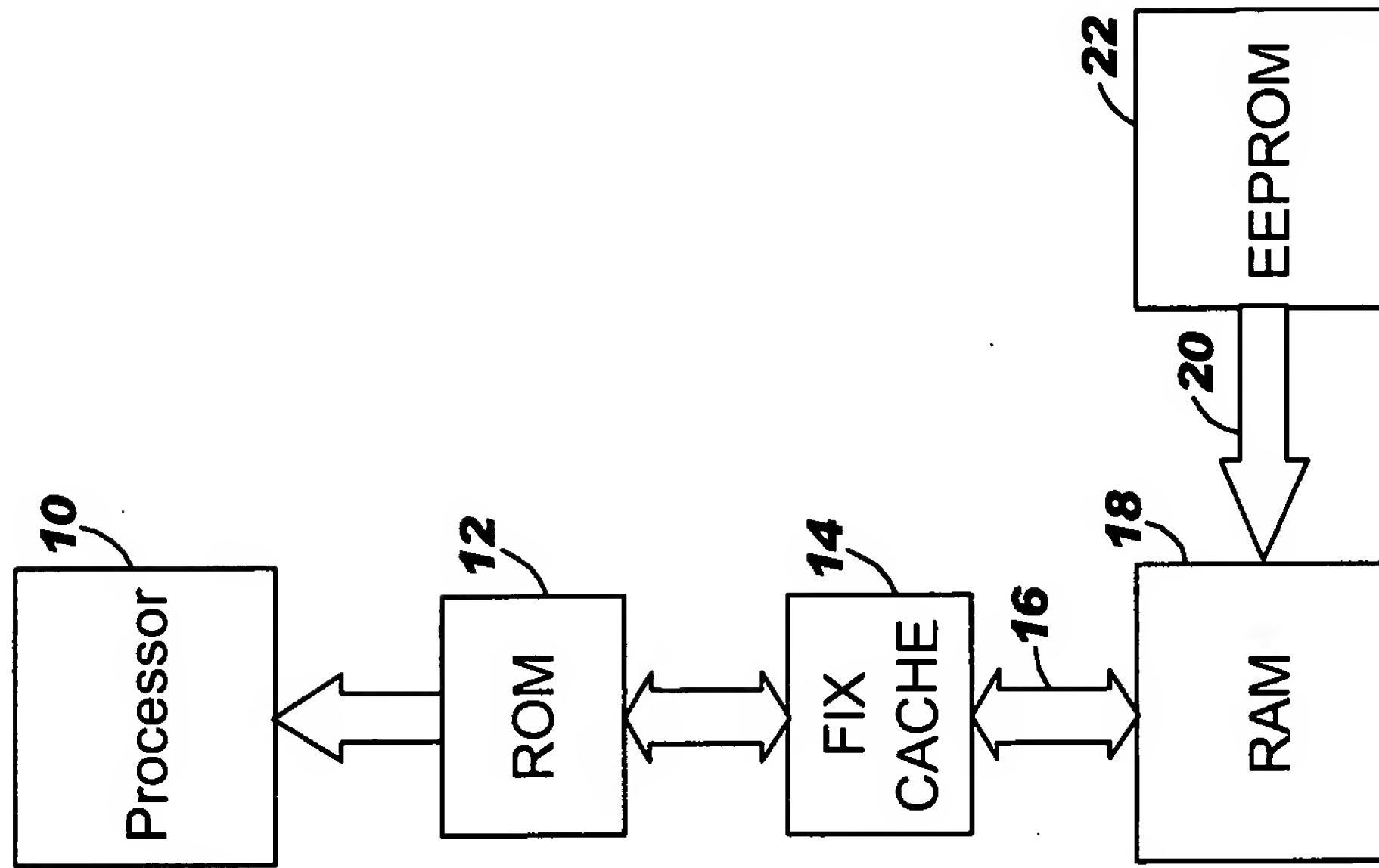


FIG. 1
Prior Art

Current Method of Fixes

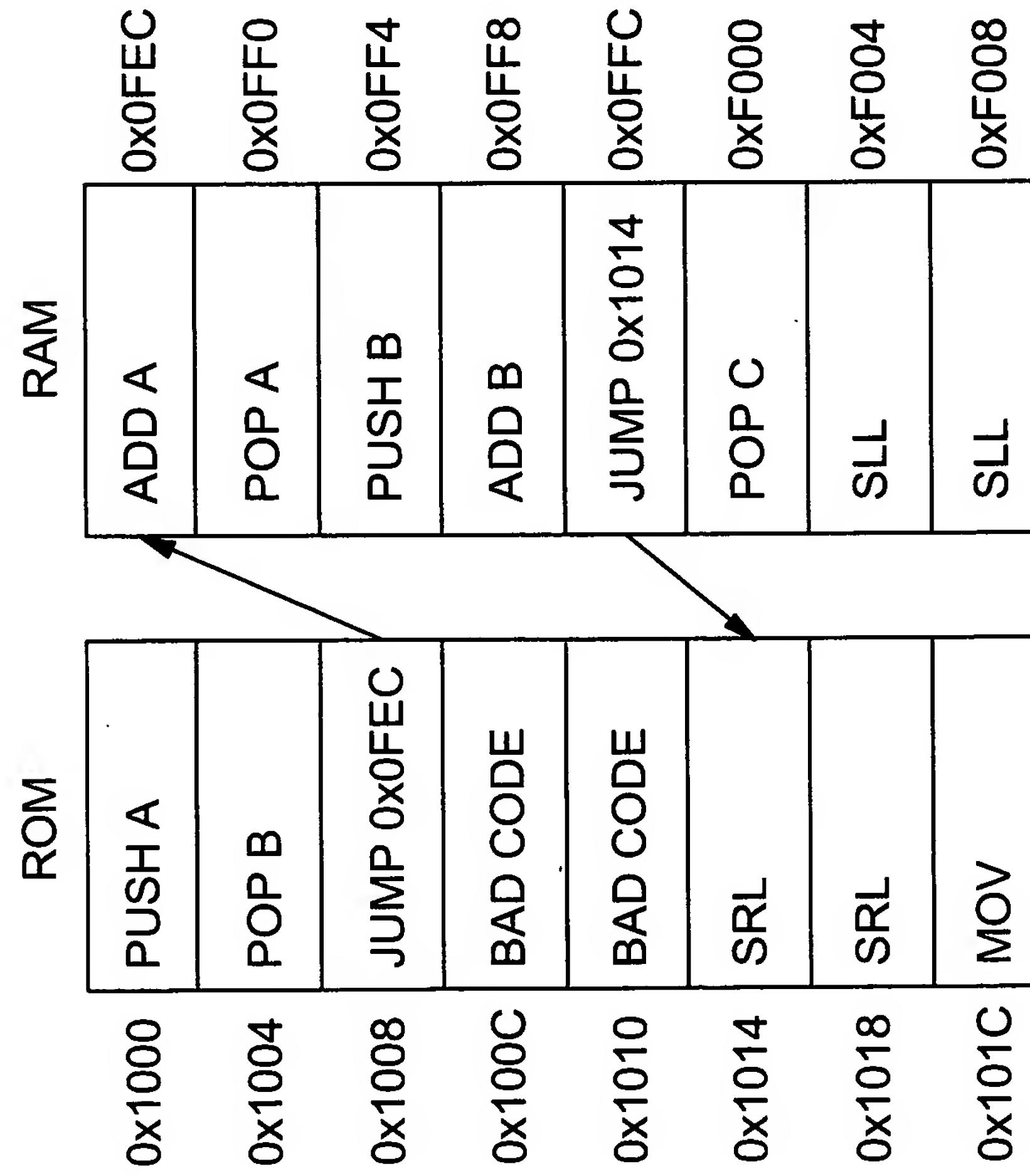


FIG. 3

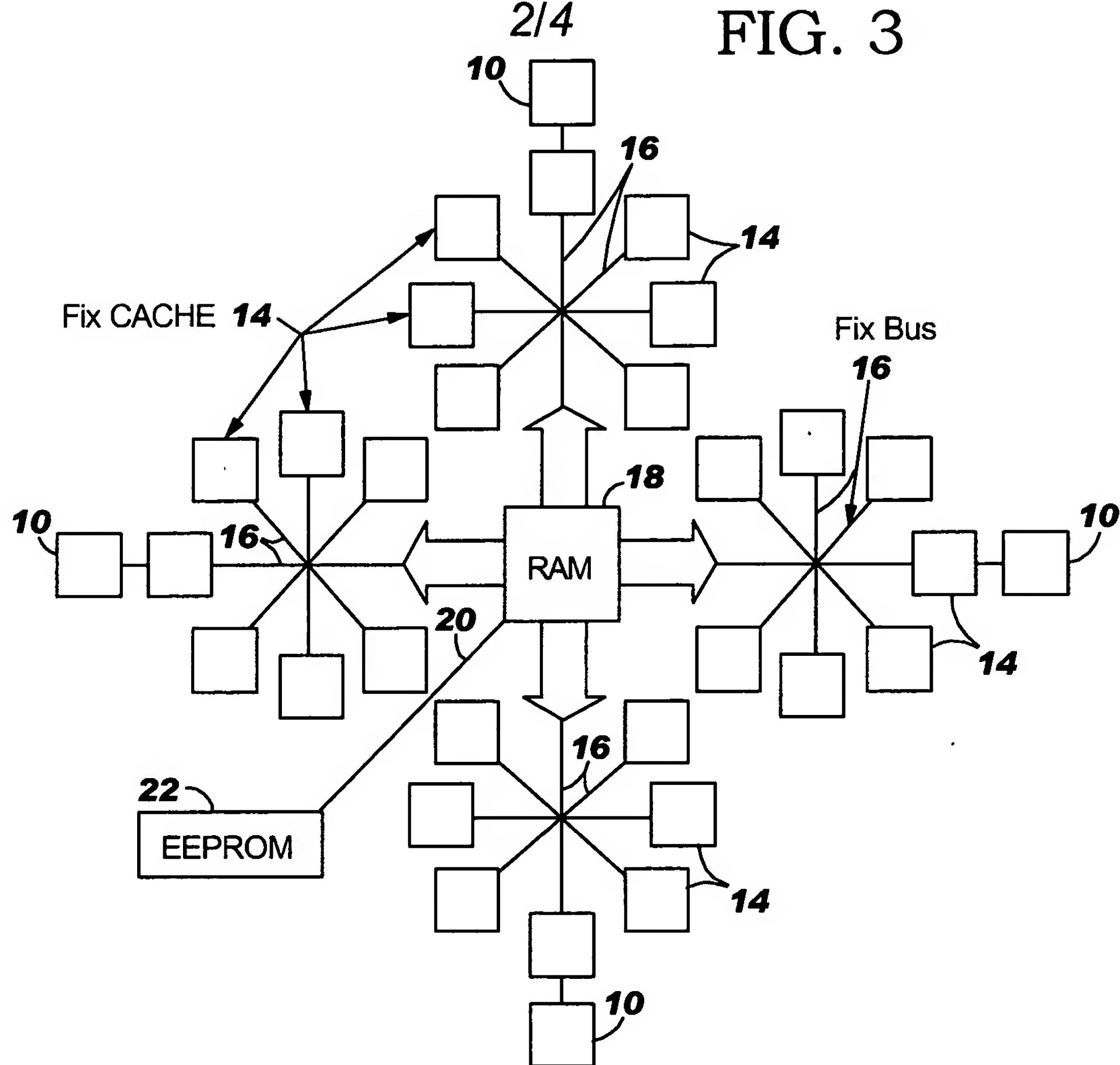
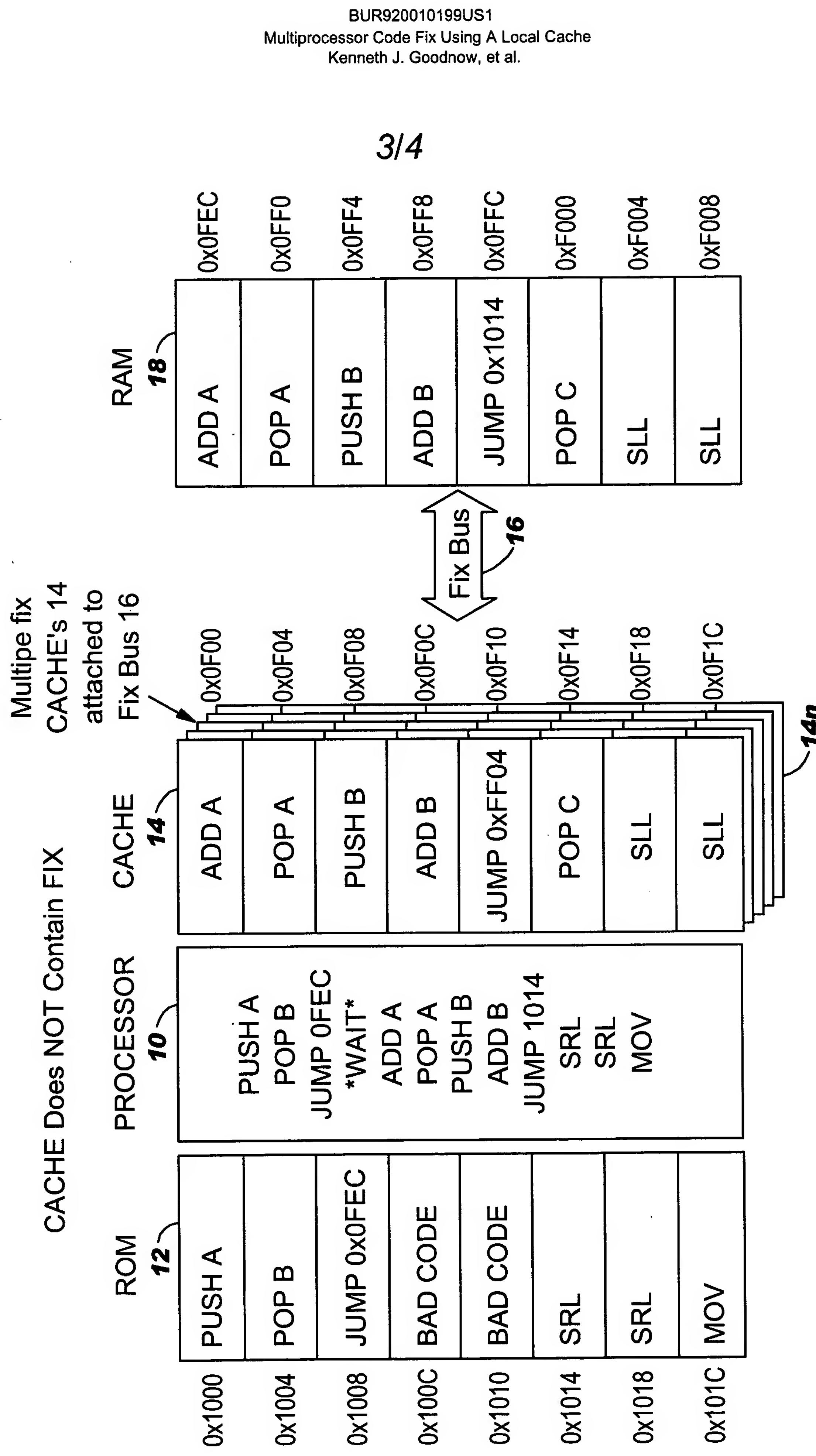


FIG. 4

CACHE Contains FIX	ROM 12	PROCESSOR 10	CACHE 14
	0x1000 PUSH A		ADD A 0x0FEC
	0x1004 POP B		POP A 0xFF0
	0x1008 JUMP 0x0FEC	PUSH A POP B JUMP 0x0FEC	PUSH B 0x0FF4
	0x100C BAD CODE	ADD A POP A PUSH B ADD B JUMP 1014	ADD B 0x0FF8
	0x1010 BAD CODE	SRL SRL MOV	JUMP 0x1014 0x0FFC
	0x1014 SRL		POP C 0xF000
	0x1018 SRL		SLL 0xF004
	0x101C MOV		SLL 0xF008

FIG. 5



4/4

FIG. 6

